



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Scott A. Rosenberg et al. § Art Unit: 2673
Serial No.: 09/365,363 §
Filed: July 30, 1999 § Examiner: Nitin Patel
For: Liquid Crystal Over Semiconductor § Atty Docket: ITL.0219US
Display With On-Chip Storage § P7127

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Commissioner for Patents
Washington DC 20231

Technology Center 2600

REQUEST FOR WITHDRAWAL OF NOTICE OF ABANDONMENT

Sir:

In Paper No. 13, it is indicated that no response was received to the office action mailed April 23, 2002.

A response, a copy of which is attached, was filed on May 21, 2002. Please note that there is a certificate of service indicating that the document was mailed on May 21, 2002 and a postcard was sent with the document.

The postcard was stamped received June 6, 2002, indicating that the United States Patent and Trademark Office received the response. A copy of the postcard is also attached.

In view of the situation, it is respectfully requested that the indication of abandonment be withdrawn.

Respectfully submitted,

Date: January 15, 2003

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Date of Deposit: <u>January 15, 2003</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents Washington DC 20231.
Cynthia Hayden

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Applicant: Scott A. Rosenberg et al. § Group Art Unit: 2673
Serial No.: 09/365,363 § Examiner: N. Patel
Filed: July 30, 1999 § Atty. Dkt. No.: ITL.0219-US
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REPLY TO PAPER NO. 12

Technology Center 2600

Sir:

In response to the office action mailed April 23, 2002, reconsideration is requested in view of the following remarks.

REMARKS

Claim 13 calls for a refresh circuit coupled to a memory array and a pixel array. The refresh circuit refreshes both the memory array and the pixel array.

The cited reference to Cheng teaches the conventional procedure of refreshing the pixel array. However, there is no discussion whatsoever that the same circuitry that refreshes the pixel array refreshes the memory array. To the contrary, in Figure 4, all that the graphics controller 430 sends to the image buffer memory 410 is the memory address on the line 470. There is no indication that the actual data is provided back to the image buffer memory 410 that would be necessary in order to refresh the image buffer memory 410.

To the contrary, it appears that the image buffer memory 410 is preferably a static random access memory which, of course, would not need to be refreshed whatsoever. See column 5, lines 6-12. Cheng does suggest that in an alternative embodiment a DRAM memory might be used. However, since in the preferred embodiment which is discussed in the ensuing sections, no refresh would be required, there is no teaching of any kind of refresh of the buffer

Date of Deposit: 05-21-02

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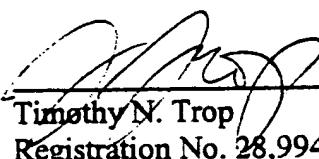
Cynthia L. Hayden

memory. Therefore, there can be no hint, suggestion, or teaching of any kind that both the pixel memory and the buffer memory could be refreshed by the same refresh circuit.

Therefore, claim 13 should be in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested.

Respectfully submitted,

Date: 5/21/02



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Attorney Docket No.: ITL_0219US (P7127)
Date: May 21, 2002

TNT/ch

The Patent and Trademark Office date stamp sets forth the receipt date of the following documents in the below referenced patent application identified as follows:

Applicant(s): SCOTT A. ROSENBERG AND ANTHONY C. MILLER
Serial No.: 09/365,363
Filing Date: July 30, 1999
Title: LIQUID CRYSTAL OVER SEMICONDUCTOR DISPLAY WITH ON-CHIP STORAGE

1. Reply to Paper No. 12; and
2. Postcard.



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